



CUSTOMER NO. 23932

Docket No.: 61181-00012USPX  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Fabio Campi et al.

Application No.: 10/770,122

Confirmation No.: 7254

Filed: February 2, 2004

Art Unit: 2183

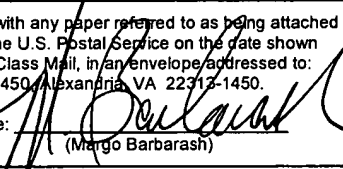
For: **DIGITAL ARCHITECTURE FOR  
RECONFIGURABLE COMPUTING IN  
DIGITAL SIGNAL PROCESSING**

Examiner: E. Coleman

**INFORMATION DISCLOSURE STATEMENT (IDS)**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as First Class Mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Dated: September 22, 2006 Signature: 

(Margo Barbarash)

Dear Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is filed more than three months after the U.S. filing date, OR more than three months after the date of entry of the national stage of a PCT application, AND after the mailing date of the first Office Action on the merits, whichever occurs first, but before the mailing date of a Final Office Action or Notice of Allowance (37 CFR 1.97(c)).

Applicant has not submitted copies of each cited U.S. patent and U.S. patent application in accordance with 37 CFR 1.98(a)(2). Applicant submits herewith copies of any cited non-patent documents and foreign patent documents in accordance with 37 CFR 1.98(a)(2).

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In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists. In accordance with 37 CFR 1.97(h), the filing of this Information Disclosure Statement shall not be construed to be an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

It is submitted that the Information Disclosure Statement is in compliance with 37 CFR 1.98 and the Examiner is respectfully requested to consider the listed references.

Our check in the amount of \$1,200.00 covering the fees set forth in 37 CFR 1.17(a)(3) and 1.17(p) is enclosed. The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 10-0447, under Order No. 61181-00012USPX.

Dated: September 22, 2006

Respectfully submitted,

By 

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CUSTOMER NO. 23932

IN LIEU OF PTO-SB/08.

<b>Substitute for form 1449A/B/PTO</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)				<b>Complete if Known</b>	
				Application Number	10/770,122-Conf. #7254
				Filing Date	February 2, 2004
				First Named Inventor	Fabio Campi
				Art Unit	2183
				Examiner Name	E. Coleman
Sheet	1	of	1	Attorney Docket Number	61181-00012USPX

U.S. PATENT DOCUMENTS				
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		Number-Kind Code <sup>2</sup> (if known)		
	A1*	US-6,467,009	10-15-2002	Winegarden, et al.
	A2*	US-6,026,481	02-15-2000	New, et al.
	A3*	US-5,968,161	10-19-1999	Southgate

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document		T <sup>6</sup>
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)	MM-DD-YYYY			

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. \* CITE NO.: Those application(s) which are marked with an single asterisk (\*) next to the Cite No. are not supplied (under 37 CFR 1.98(a)(2)(iii)) because that application was filed after June 30, 2003 or is available in the IFW. <sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T <sup>2</sup>
	C1	Krishna V. Palem and Surendranath Talla; "Adaptive Explicitly Parallel Instruction Computing," Proceedings of the 4th Australasian Computer Architecture Conference, Auckland, New Zealand, January 18-21, 1999, pps. 61-73.		
	C2	Ralph D. Wittig and Paul Chow, "OneChip: An FPGA Processor with Reconfigurable Logic," University of Toronto, Department of Electrical and Computer Engineering, Toronto, Ontario, Canada; pps. 126-135, 1996 IEEE; 0-8186-7548-9/96.		
	C3	Callahan, et al., "The Garp Architecture and C Compiler," <i>Configurable Conspiracy</i> , University of California, Berkeley, 2000 IEEE, 0018-9162/00, pps. 6269, April 2000.		
	C4	Rahul Razdan and Michael D. Smith, "A High-Performance Microarchitecture and Hardware-Programmable Functional Units," 1994 ACM 0-89791-707-1/94/0011, pps. 172-180, March		
	C5	Zhi Alex Ye, et al., "CHIMAERA: A High-Performance Architecture with a Tightly-Coupled Reconfigurable Unit," ISCA 2000 Vancouver BC Canada, ACM 2000 1-58113-232-8/00/6, pps. 225-235.		
	C6	Ricardo E. Gonzales, "Xtensa: A Configurable and Extensible Processor," IEEE Micro, March-April 2000, pps.60-70; 0272-1732/00, 2000 IEEE.		
	C7	Kastrup, et al., "ConCISE: A Compiler-Driven CPLD-Based Instruction Set Accelerator," <i>Philips Research Laboratories</i> , date unknown.		
	C8	S. Vassiliadis, et al, "The MOLEN $\mu$ -Coded Processor," FPL 2001, LNCS 2147, pps. 275-285, 2001.		
	C9	European Search Report, EP 03 42 5055, dated November 2003.		

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

Examiner Signature		Date Considered	
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